



2815

PATENT APPLICATIONIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Ahn et al. /

DOCKET NO. 8028-17  
(SPX200110-0008US)

SERIAL NO: 10/054,700 /

GROUP: Art Unit 2815

FILED: January 17, 2002 /

EXAMINER: Clark, Sheila V.

FOR: PAD ARRANGEMENT IN SEMICONDUCTOR MEMORY DEVICE  
AND METHOD OF DRIVING SEMICONDUCTOR DEVICE#5  
election  
Chen  
7/10/03Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450RECEIVED  
JUL 10 2003  
TECHNOLOGY CENTER 2800RESPONSE TO RESTRICTION REQUIREMENT

Sir:

In response to the Office Action dated June 3, 2003, applicant provisionally elects the claims of Group I (claim nos. 1-10), with traverse. While the device of Group I and the method of Group II may be distinct for the reasons set forth by the Examiner, applicant believes that simultaneous examination will not present an undue burden. For example, the claims of Group I are drawn to a semiconductor device and the claims of Group II are drawn to a method of making same. The two Groups of claims include subject matters that are classified commonly in class 463 and subclass 17. Under such circumstances, the Examiner is encouraged to maintain all claims in the same application. See, MPEP § 803.

CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail, postpaid in an envelope, addressed to the: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on July 2, 2003.

Dated: 7/2/03  
Frank Chau

Early and favorable consideration of this application are earnestly solicited.

Respectfully submitted,

By:

  
Frank Chau

Reg. No. 34,136

Attorney for Applicants

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